Amendments to the Claims

This listing of claims replaces all prior versions, and listings, of claims in the application.

Listing of Claims

~~Claim~~ 1. (Currently amended)[[:]] ~~Assembly~~ An assembly comprising a mobile telephone ~~supplied~~ powered by a self-contained power source, an auxiliary memory and a charger ~~arranged so as to charge the~~ provided for charging said power source, ~~the~~ said telephone being provided with a memory arranged to store therein ~~the~~ data of ~~the~~ an operator and data introduced by an owner of the telephone, ~~the~~ said memory and auxiliary memory ~~in~~ each ~~case~~ being equipped with a read and write ~~device~~ member to allow ~~the~~ a reading and a writing of data in the respective memory, ~~the~~ said auxiliary memory being associated with ~~the~~ said charger, ~~the~~ said charger being provided with ~~initialisation~~ initialization means connected to ~~the~~ said read and write ~~devices~~ member, ~~the~~ said ~~initialisation~~ initialization means being arranged to detect a charging of ~~the~~ said power source and produce an ~~initialisation~~ initialization signal after detection of such a charging, ~~characterised in that the initialisation~~ said initialization means ~~are~~ being arranged to activate ~~the~~ said read ~~device~~ member of ~~the~~ said memory and ~~the~~ said write ~~device~~ member of ~~the~~ said auxiliary memory under the

control of ~~the initialisation~~ said initialization signal in order to read ~~the~~ said data of ~~the~~ said memory and to write in ~~the~~ said auxiliary memory at least these data of ~~the~~ said memory which are not yet recorded in the auxiliary memory.

2. (Canceled)

~~Claim~~ 3. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that~~ wherein an identification code is stored in the memories and ~~in that the initialisation~~ said initialization means comprise a verification element arranged to compare, under the control of ~~the initialisation~~ said initialization signal, ~~the~~ codes stored in ~~the~~ said memory and the auxiliary memory ~~or respectively the first and second memory~~ in order to produce a ~~neutralisation~~ neutralization signal in the event of a non-match of ~~the~~ said identification codes compared with each other, ~~the~~ said activation of ~~the~~ said read and write ~~device~~ member being ~~neutralised~~ neutralized under the control of ~~the neutralisation~~ said neutralization signal.

~~Claim~~ 4. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means are arranged so as to activate ~~the~~ said read ~~device~~ member of ~~the~~ said auxiliary memory ~~or~~

~~respectively of the first memory~~ under the control of ~~the initialisation~~ said initialization signal in order to read ~~the~~ said data of these memories, ~~the~~ said ~~initialisation~~ initialization means comprising a comparator arranged so as to receive said data read in ~~the~~ said respective memories, after activation of ~~the~~ said read ~~devices~~ member, and to compare with each other ~~the~~ said data stored in the first and second memories ~~or respectively the memory and the auxiliary memory~~ and to mark ~~on the basis of~~ based on the comparison the data of ~~the second memory or respectively of~~ the memory which are not stored in ~~the first memory or respectively~~ the auxiliary memory and to store in ~~the first memory or respectively~~ the auxiliary memory only the data marked.

~~Claim~~ 5. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means are arranged to delete the content of the auxiliary memory ~~or respectively the first memory~~ under ~~the~~ control of ~~the initialisation~~ said initialization signal.

~~Claim~~ 6. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means are provided with a counter having an input for receiving ~~the initialisation~~ said

initialization signal, ~~the~~ said counter being arranged to increment a counting amount after reception of ~~the initialisation~~ said initialization signal and to produce a counting signal when the counting ~~level~~ amount has reached a predetermined threshold and a stop signal when ~~this~~ said counting ~~level~~ amount has not reached ~~the~~ said threshold, ~~the~~ said ~~initialisation~~ initialization means being arranged to ~~neutralise the~~ neutralize said activation of ~~the~~ said read and write ~~devices~~ member under ~~the~~ control of ~~the~~ said stop signal and to ~~initialize the~~ initialize said counting ~~level~~ amount under ~~the~~ control of ~~the~~ said counting signal.

~~Claim~~ 7. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means are provided with a transmitter arranged to transmit a message indicating a writing in ~~the~~ said auxiliary memory ~~or respectively the first memory~~ when data are written ~~in these~~ therein.

~~Claim~~ 8. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means comprise an activation key which can be activated by a user, ~~the~~ said activation key being arranged to produce an activation signal after having been activated, ~~the~~ said write ~~devices~~ member of ~~the~~ said memory ~~or of~~

~~the second memory~~ and ~~the~~ said read ~~devices~~ member of ~~the~~ said auxiliary memory ~~or the first memory~~ being able to be activated under ~~the~~ control of ~~the~~ said activation signal in order to allow writing in the memory ~~or the second memory~~ of the data read in the auxiliary memory ~~or the first memory~~.

~~Claim~~ 9. (Currently amended)[[:]] ~~Assembly~~ The assembly according to Claim 1, ~~characterised in that the initialisation~~ wherein said initialization means comprise a connection pin connected to a conductive wire ~~itself~~ that is connected to ~~the~~ said auxiliary memory, ~~the~~ said pin being compatible with that of ~~the~~ said telephone giving access to ~~the~~ said memory.

~~Claim~~ 10. (Currently amended)[[:]] ~~Initialisation~~ An initialization means ~~to be used in an~~ that is a component of the assembly according to Claim 1, said initialization means being configured to detect a charging of said power source and produce an initialization signal after detection of said charging.